

FIG.4B

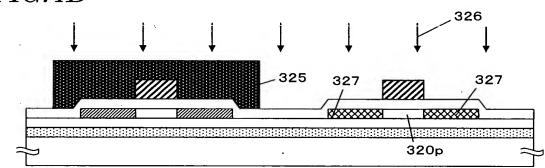


FIG.4C

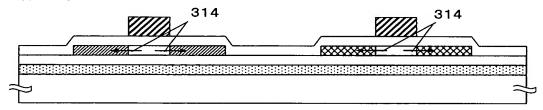
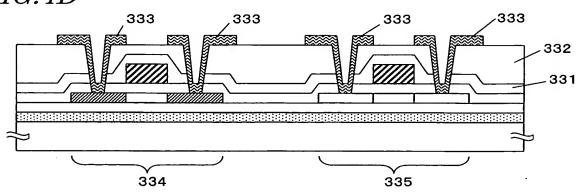
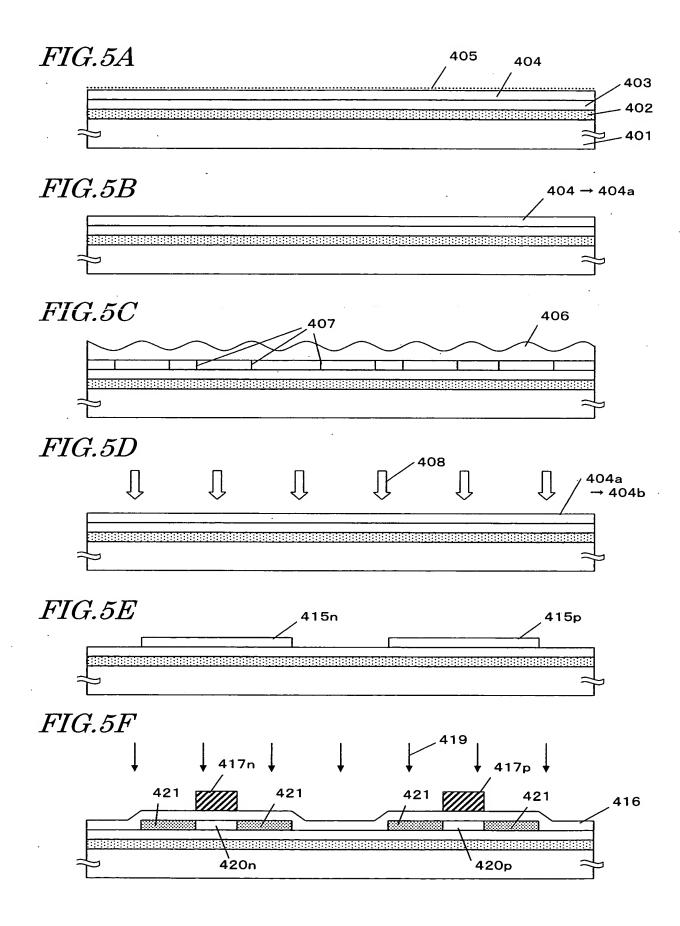
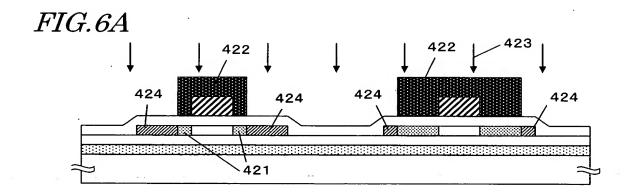
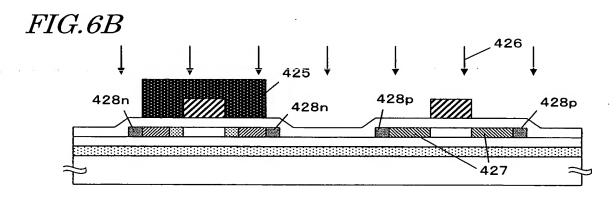


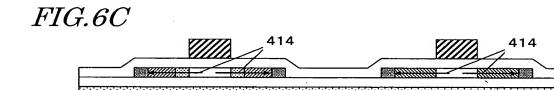
FIG.4D

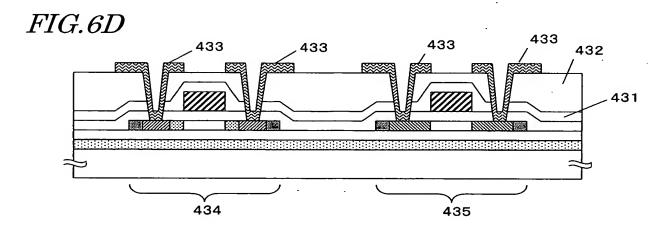


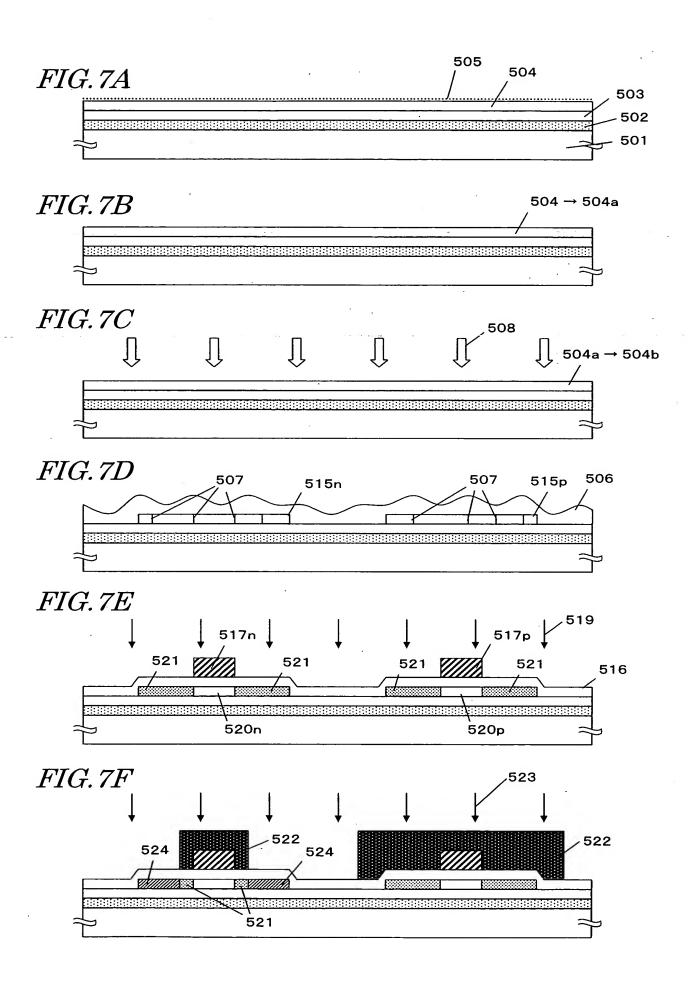


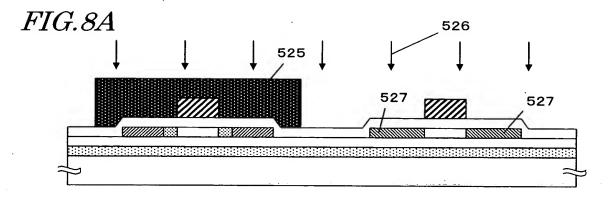












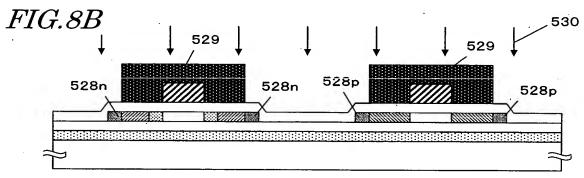
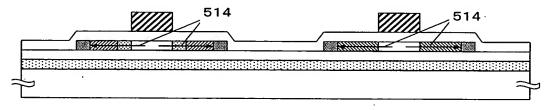
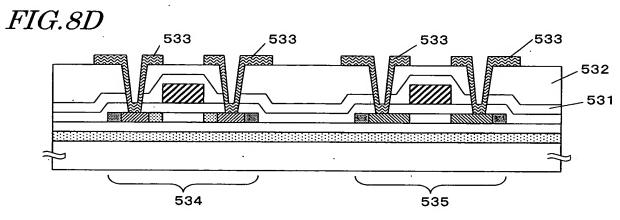
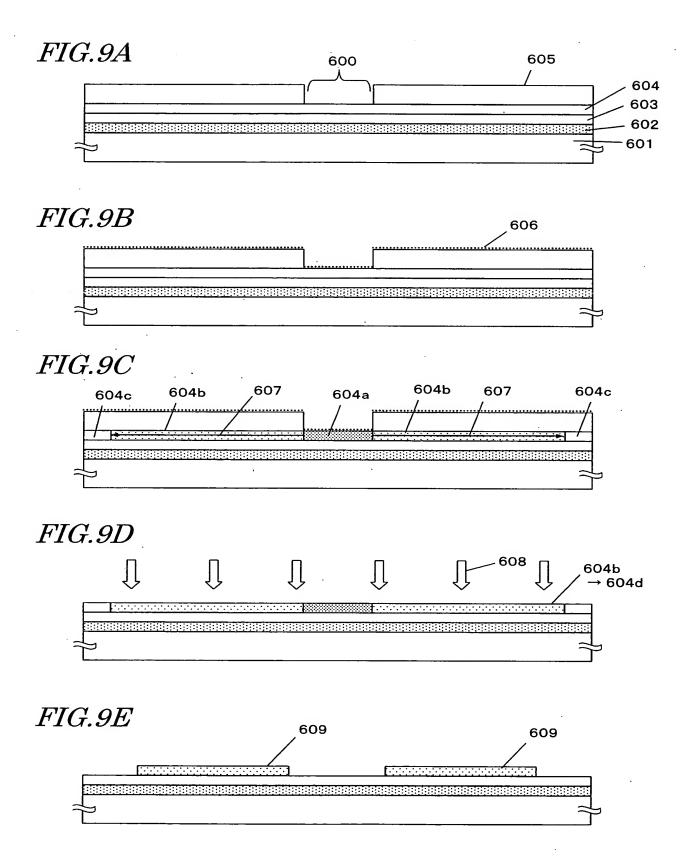
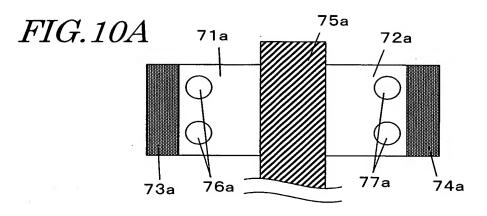


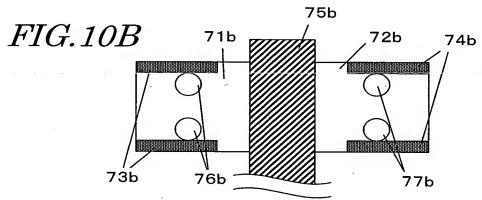
FIG.8C

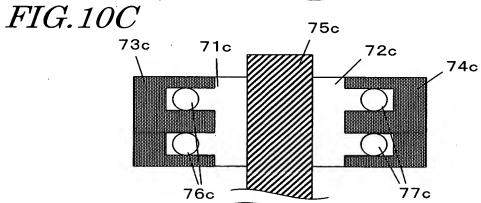


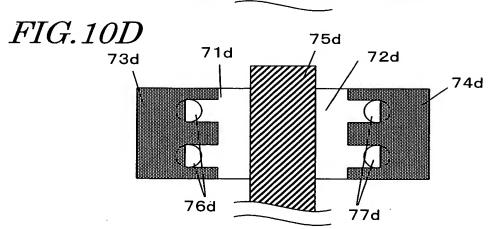


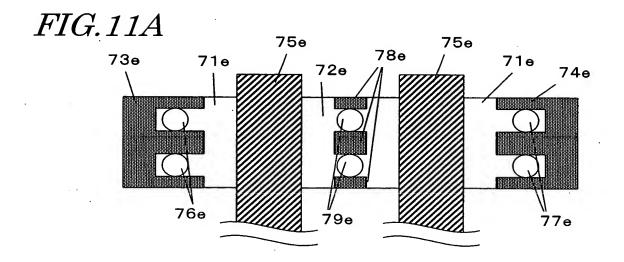


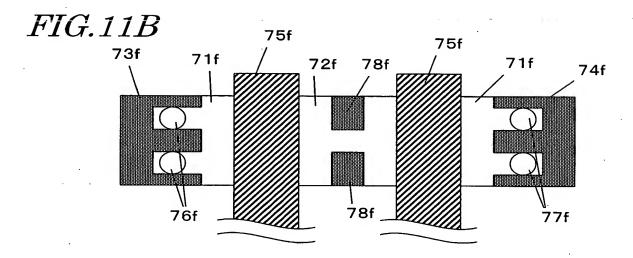












EIC 101	80 Source Driving Circuit (Source Driver)
FIG.12A	80a Shift Register
	80b Buffer
	80c Sampling Circuit
Gate Driving Circuit & (Gate Driver) Shift Register & Level Shifter & Buffer & Shifter	81 Pixel Section

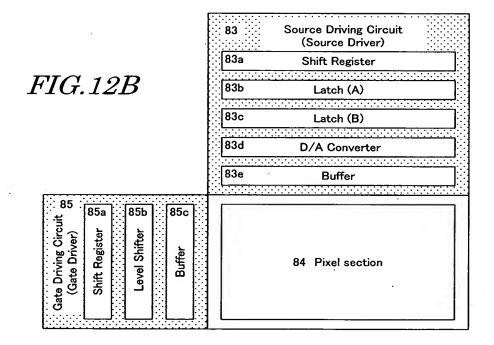
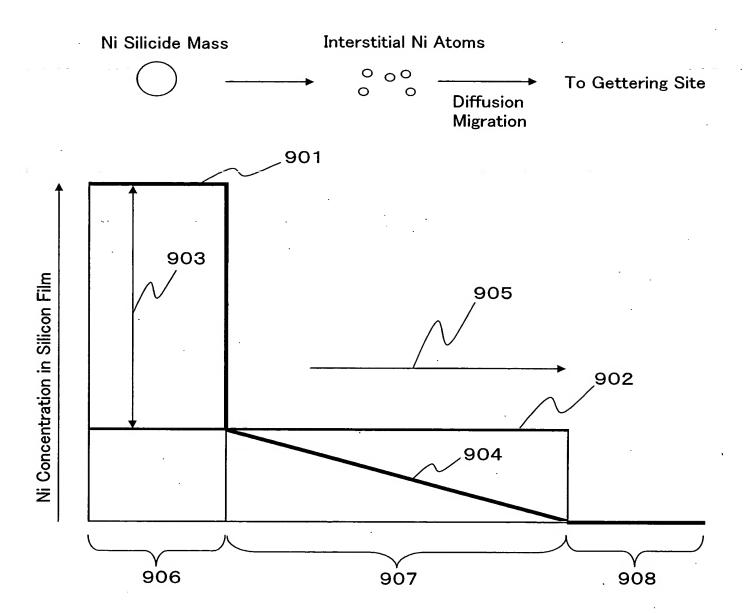
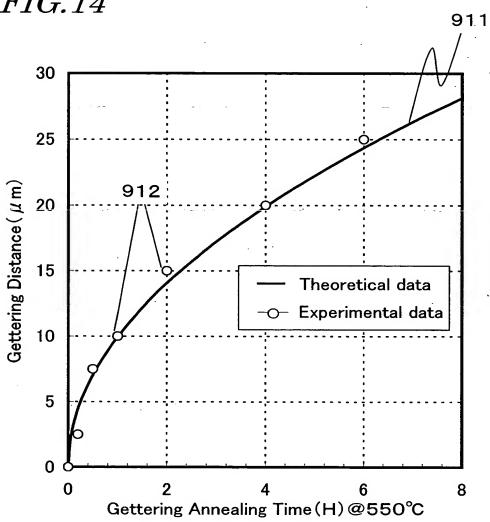
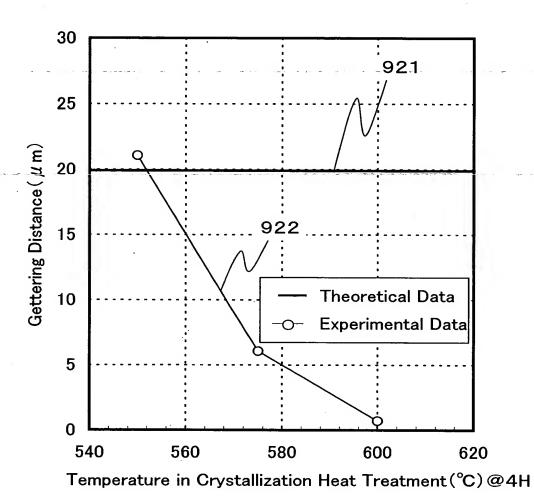


FIG.13









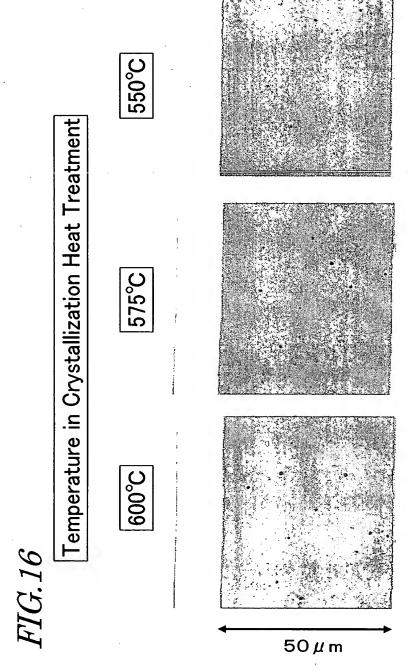
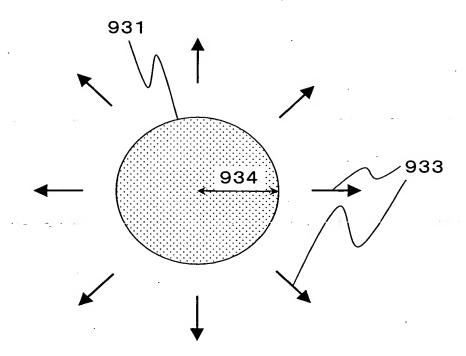
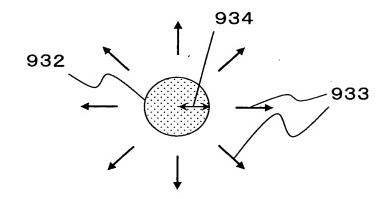


FIG.17





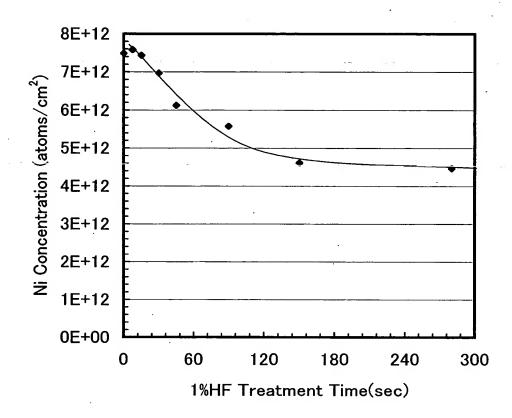
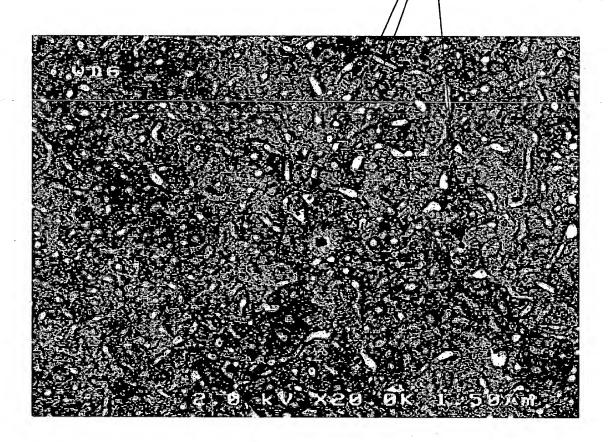


FIG.19

Minute Holes Formed After Removing Ni Silicide Masses



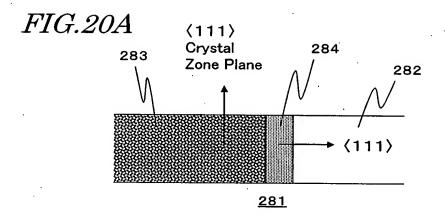


FIG.20B

